

ABSTRACT

An array substrate for a transflective liquid crystal display device includes: a gate line on a substrate; a data line crossing the gate line to define a pixel region having a transmissive portion and a reflective portion; a common line parallel to and spaced apart from the gate line; a thin film transistor connected to the gate and data lines, the thin film transistor including a gate electrode, an active layer, and source and drain electrodes; a capacitor electrode extending from the drain electrode and overlapping the common line; a reflective layer covering the common line and the thin film transistor and corresponding to the reflective portion; and a transparent electrode connected to the drain electrode and disposed in the pixel region.